

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT

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APPLICANT:
Donald B. EidsonFILING DATE:
December 15, 2000GROUP:
Unassigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
sh	AA	6,023,783	02/08/00	Divsalar et al.	714	792	05/15/97

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
				None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

sh	AB	Bahl, L. R., et al., <i>Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate</i> ; IEEE Transactions on Information Theory , March 1974, pages 284-287.
sh	AC	Benedetto, S., et al., <i>A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes</i> ; TDA Progress Report , 42-127, November 1996, pages 1-20.
sh	AD	Benedetto, S. and Montorsi, G., <i>Iterative decoding of serially concatenated convolutional codes</i> ; Electronics Letters , Vol. 32, No. 13, June 1996, pages 1186-1188.
sh	AE	Benedetto, S. and Montorsi, G., <i>Serial concatenation of block and convolutional codes</i> ; Electronics Letters , Vol. 32, No. 10, May 1996, pages 887-888.
sh	AF	Berrou, Claude, et al., <i>Near Shannon Limit Error - Correcting Coding and Decoding: Turbo-Codes (1)</i> ; IEEE , 1993, pages 1064-1070.
sh	AG	Divsalar, D. and Pollara, F., <i>Serial and Hybrid Concatenated Codes with Applications</i> ; Jet Propulsion Laboratory, California Institute of Technology , pages 1-8.
sh	AH	Divsalar, D. and Pollara, F., <i>Turbo Trellis Coded Modulation with Iterative Decoding for Mobile Satellite Communications</i> ; Jet Propulsion Laboratory, California Institute of Technology , pages 1-7.

EXAMINER:

sh Chang

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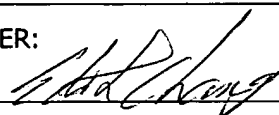
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gh	AI	Hagenauer, Joachim and Hoeher, P., <i>A Viterbi Algorithm with Soft-Decision Outputs and its Applications</i> ; Proceedings of IEEE Globecom '89 ; Dallas, Texas, November 1989; pages 47.1.1 – 47.1.7.
gh	AJ	Hoeher, Peter and Lodge, John, "Turbo DPSK": <i>Iterative Differential PSK Demodulation and Channel Decoding</i> ; IEEE Transactions on Communications , Vol. 47, No. 6, June 1999, pages 837-843.
gh	AK	Narayanan, Krishna R. and Stüber, Gordon L., <i>A Serial Concatenation Approach to Iterative Demodulation and Decoding</i> ; IEEE Transactions on Communications , Vol. 47, No. 7, July 1999, pages 956-961
gh	AL	Robertson, P., et al., <i>A Comparison of Optimal and Sub-Optimal MAP Decoding Algorithms Operating in the Log Domain</i> ; IEEE , 1995, pages 1009-1013.
gh	AM	Viterbi, Andrew J., <i>An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes</i> ; IEEE Journal on Selected Areas in Communications , Vol. 16, No. 2, February 1998, pages 260-264.

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